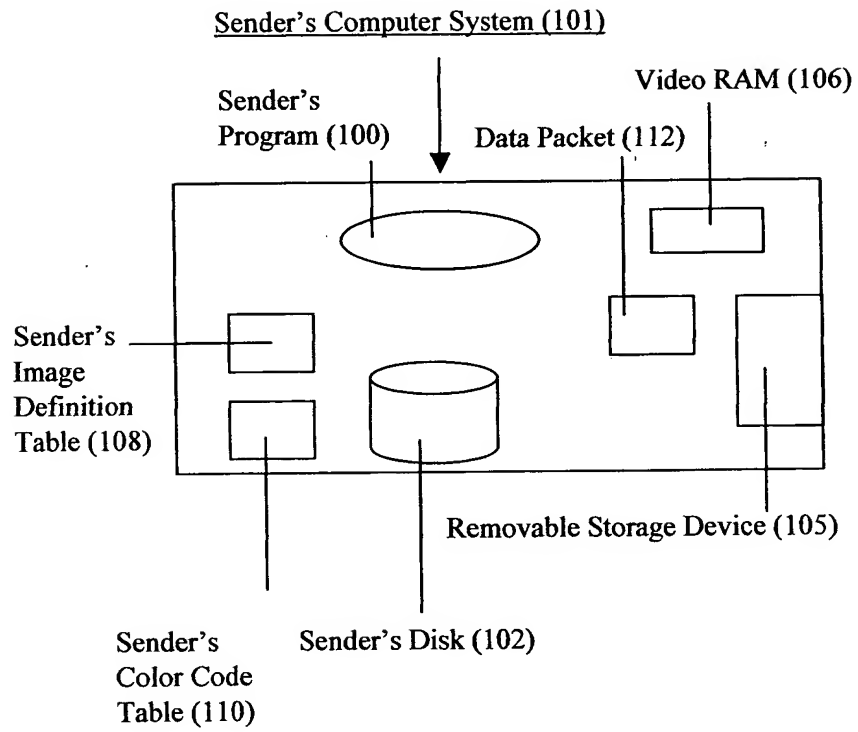


**Fig. 1**



**Fig. 1a**

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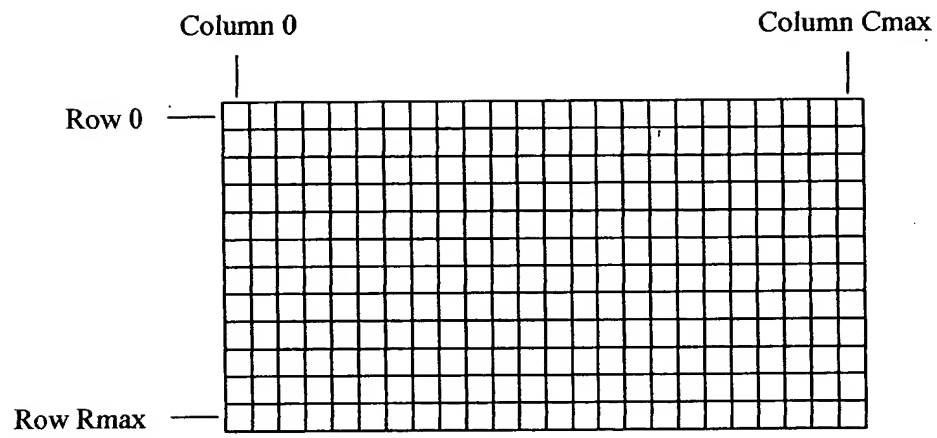


Fig. 2

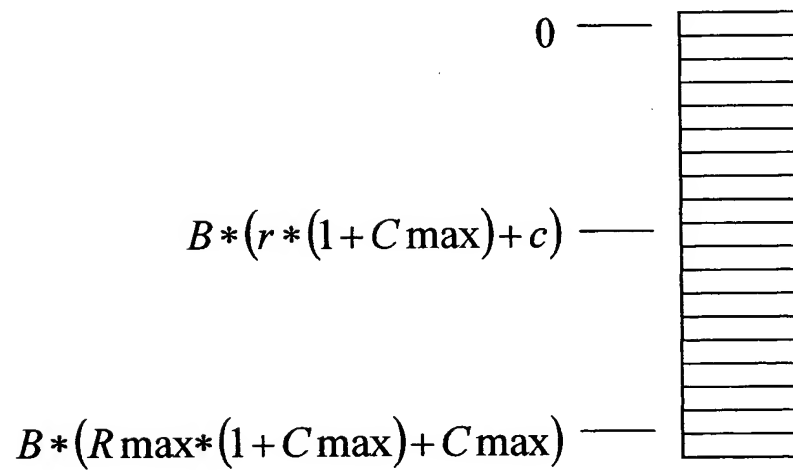


Fig. 3

<u>Image Area</u>	<u>Ordered Set of Memory Address Offsets</u>
0	{ Set 0 }
1	{ Set 1 }
2	{ Set 2 }
Amax	{ Set Amax }

Fig. 4

<u>Row</u>	<u>Red</u>	<u>Green</u>	<u>Blue</u>	<u>Color Value</u>
0	r0	g0	b0	c0
1	r1	g1	b1	c1
2	r2	g2	b2	c2
..				
..				
..				
2 <sup>N</sup> - 1				

Fig. 5

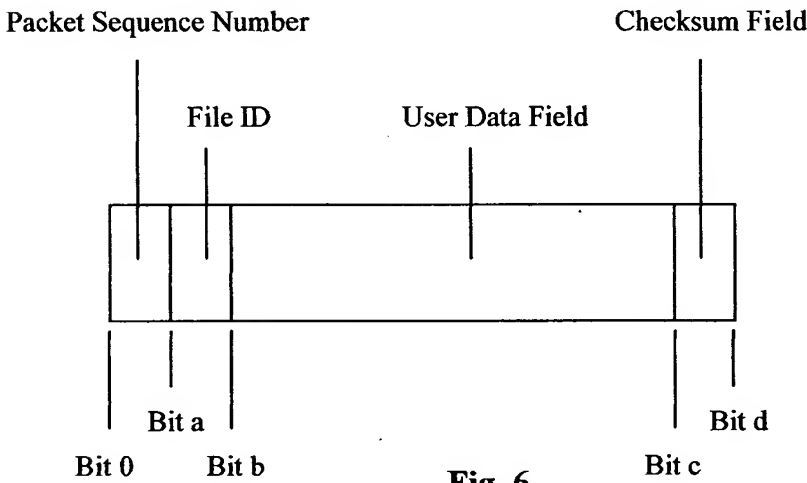


Fig. 6

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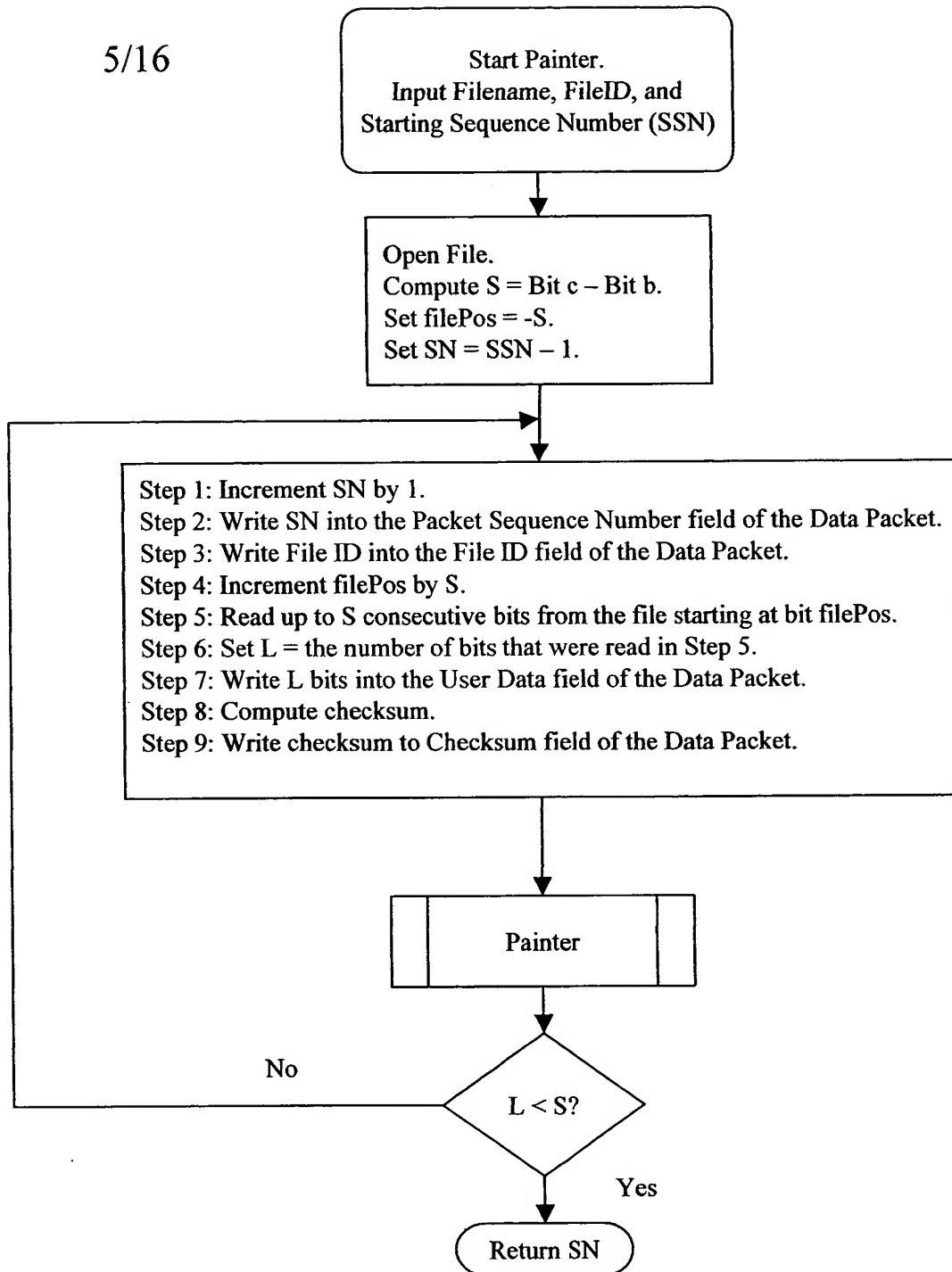


Fig. 7

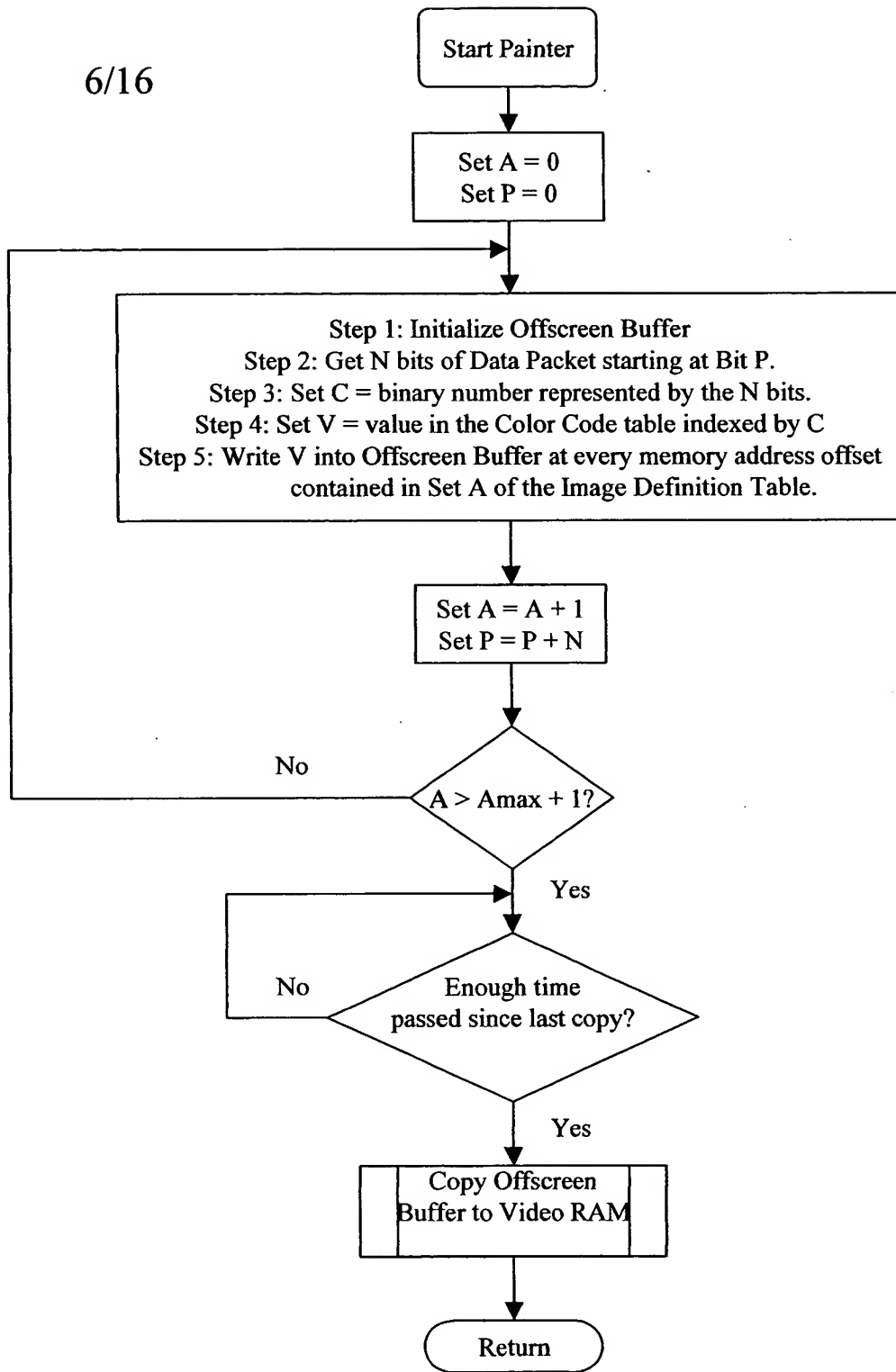


Fig. 8

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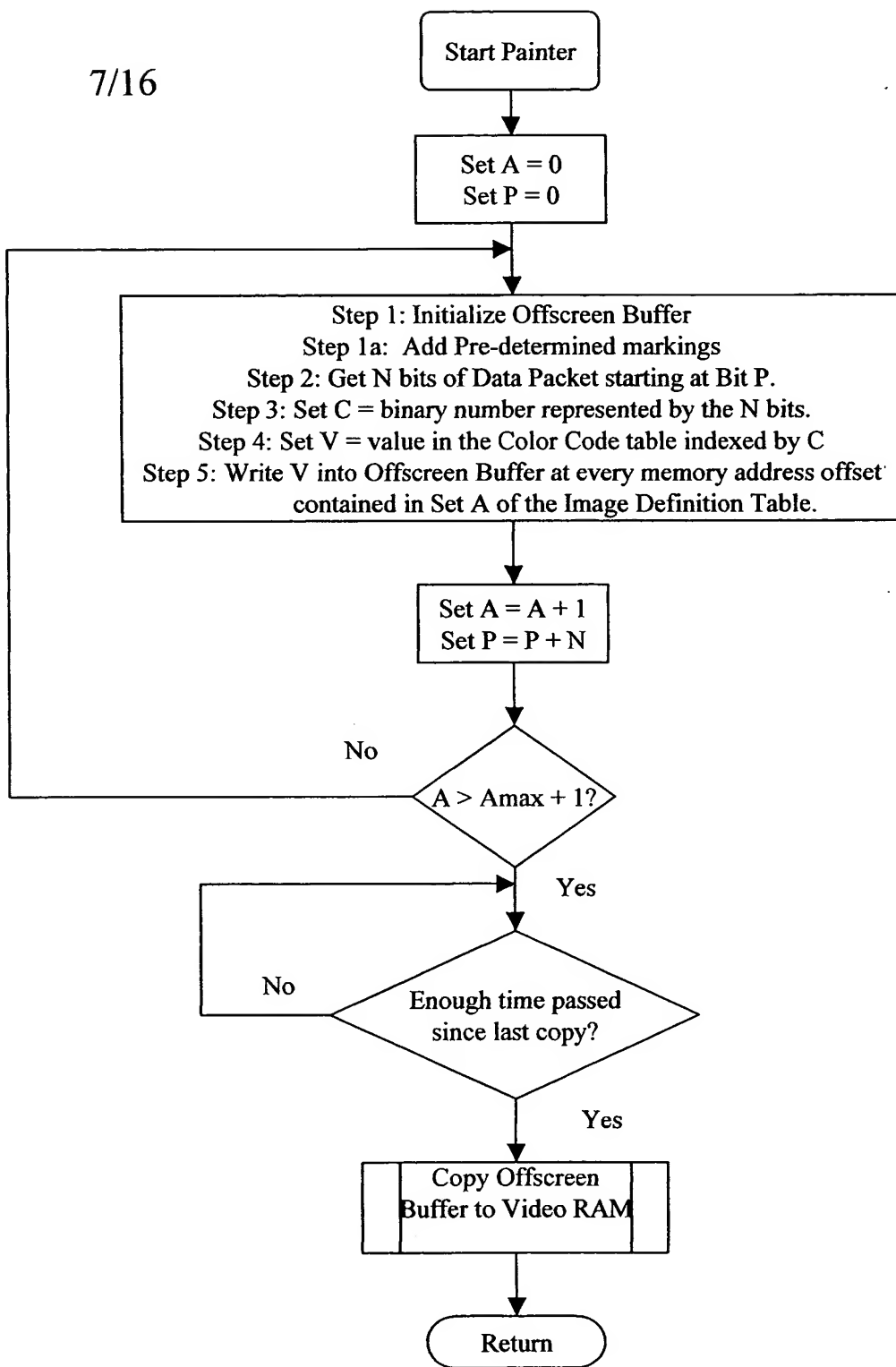


Fig. 8a

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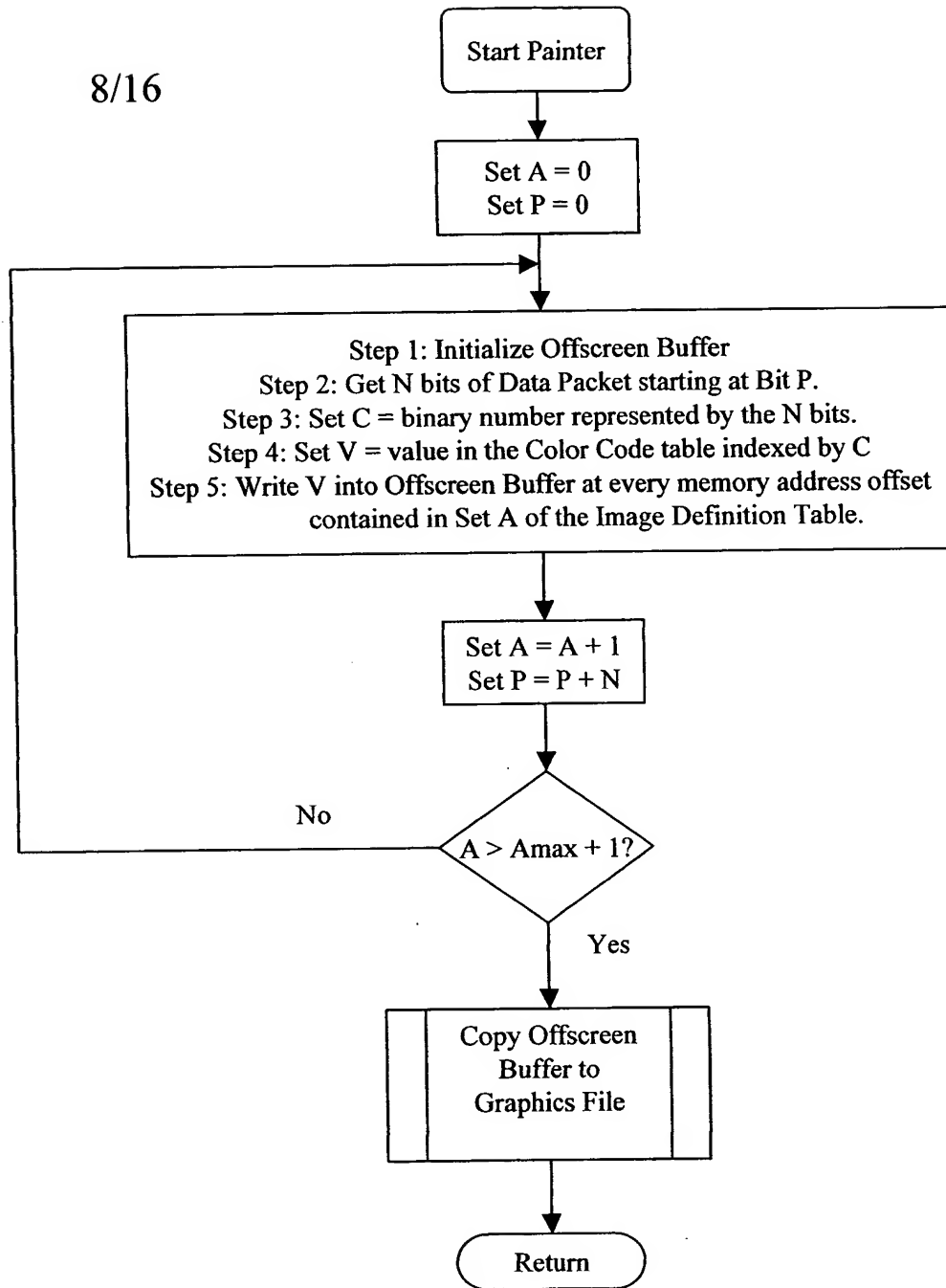
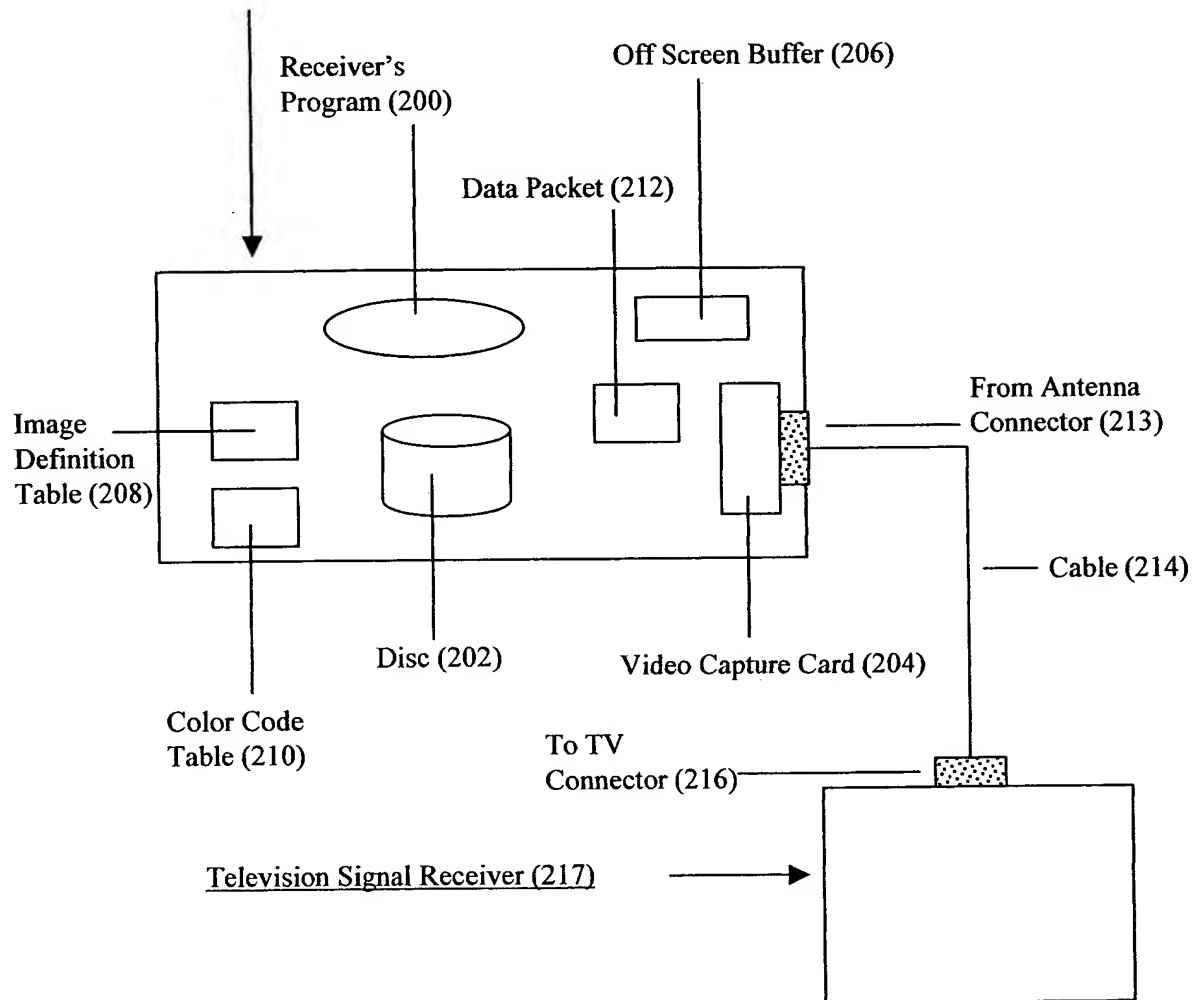


Fig. 8b



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**Fig. 9**

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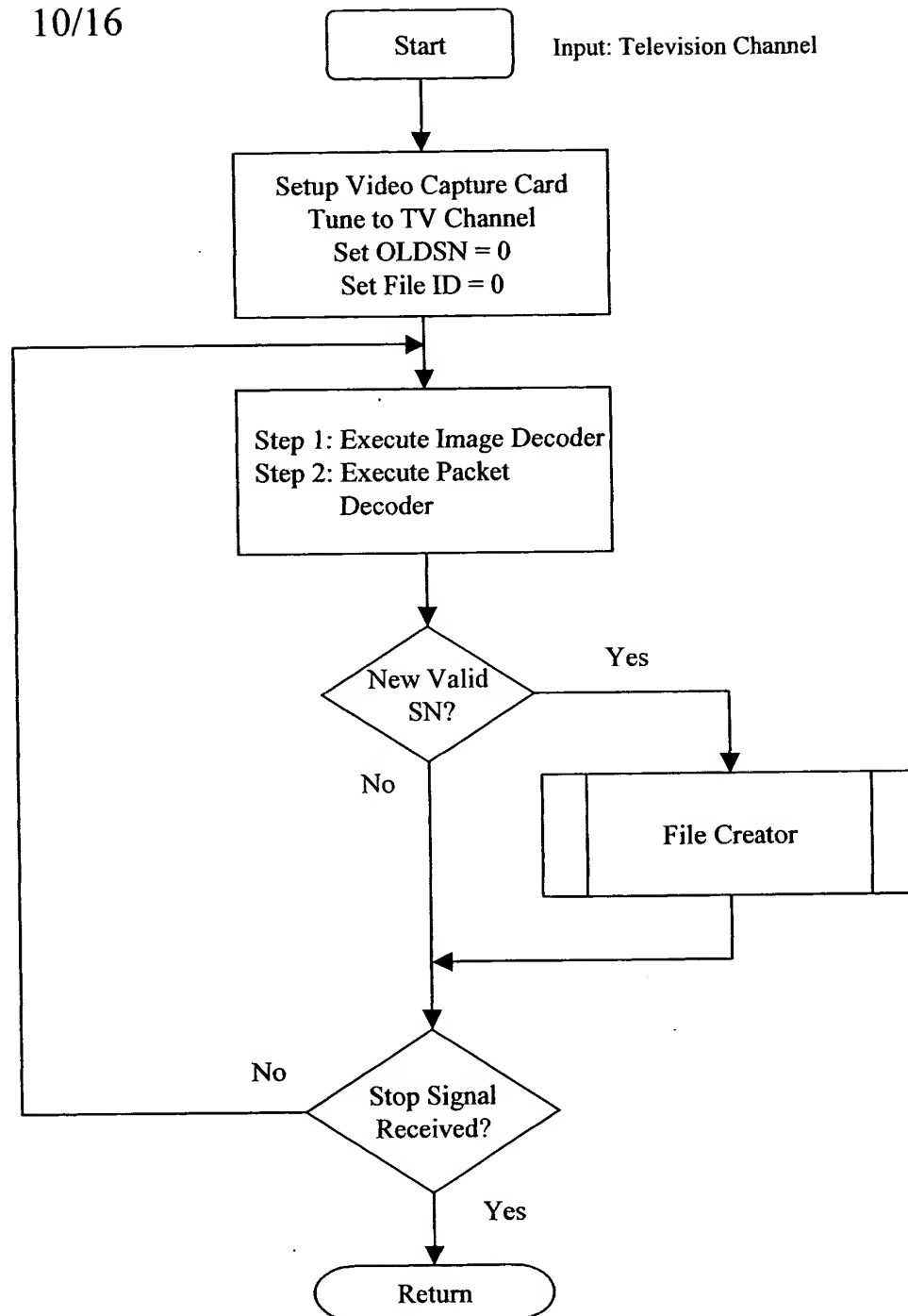


Fig. 10

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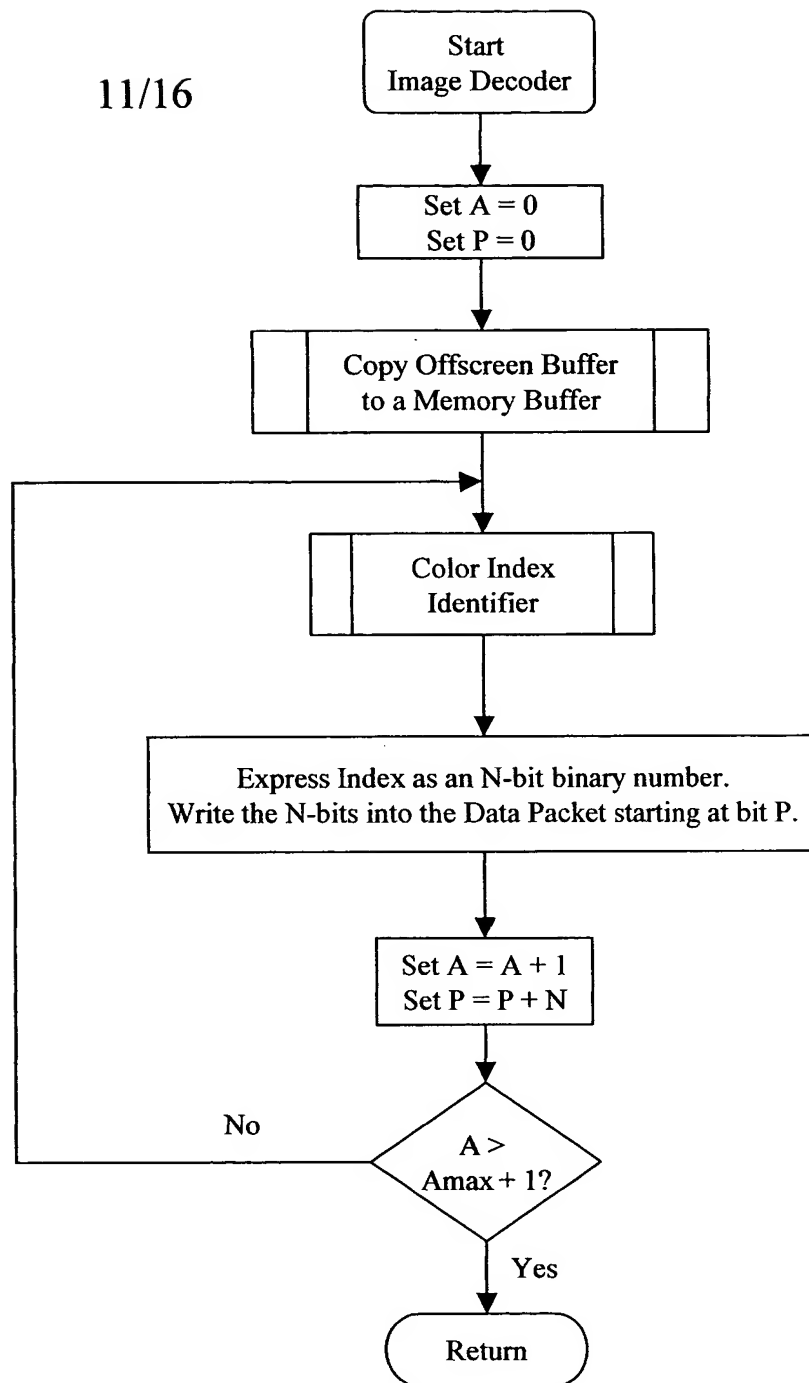


Fig. 11

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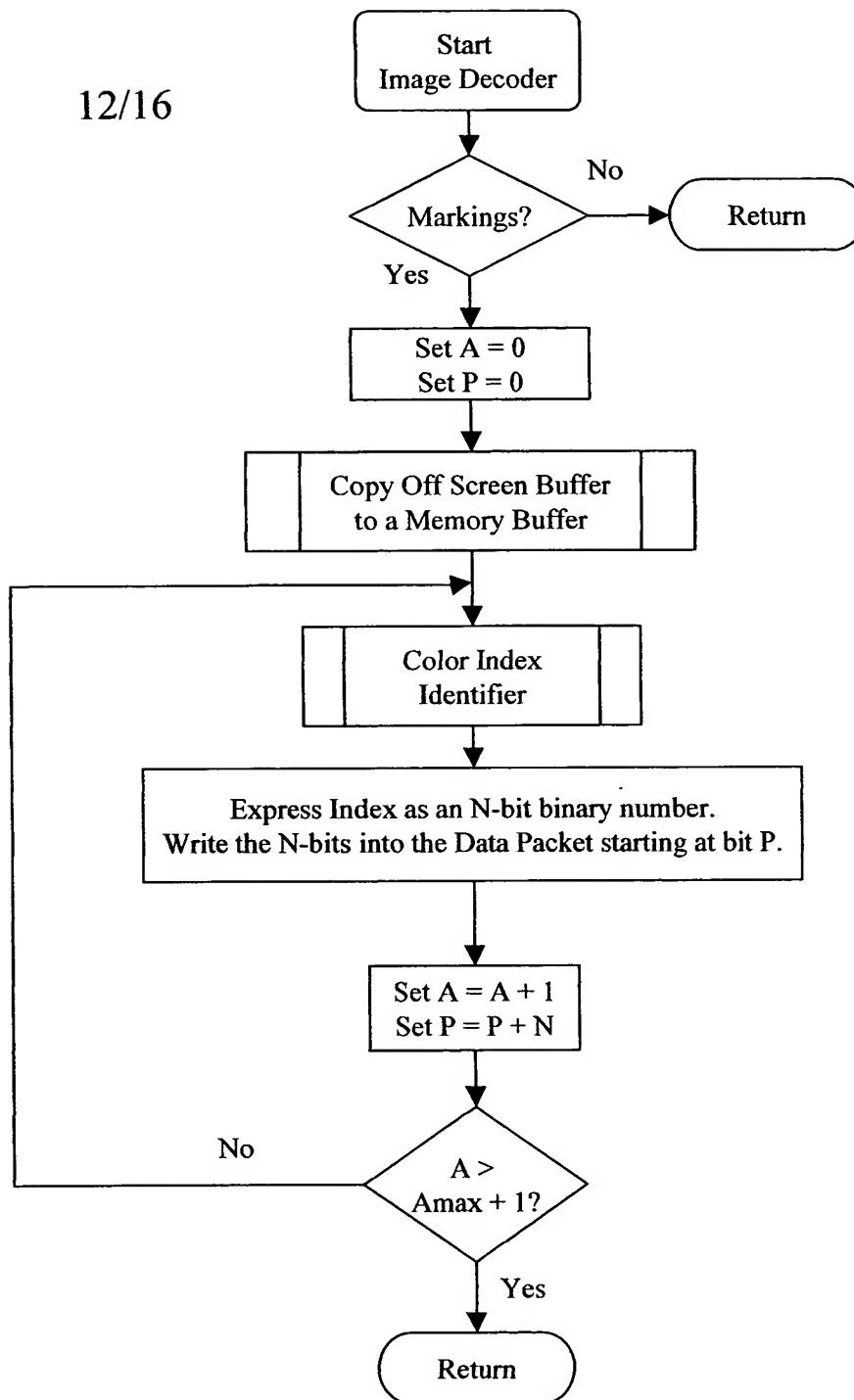


Fig. 11a

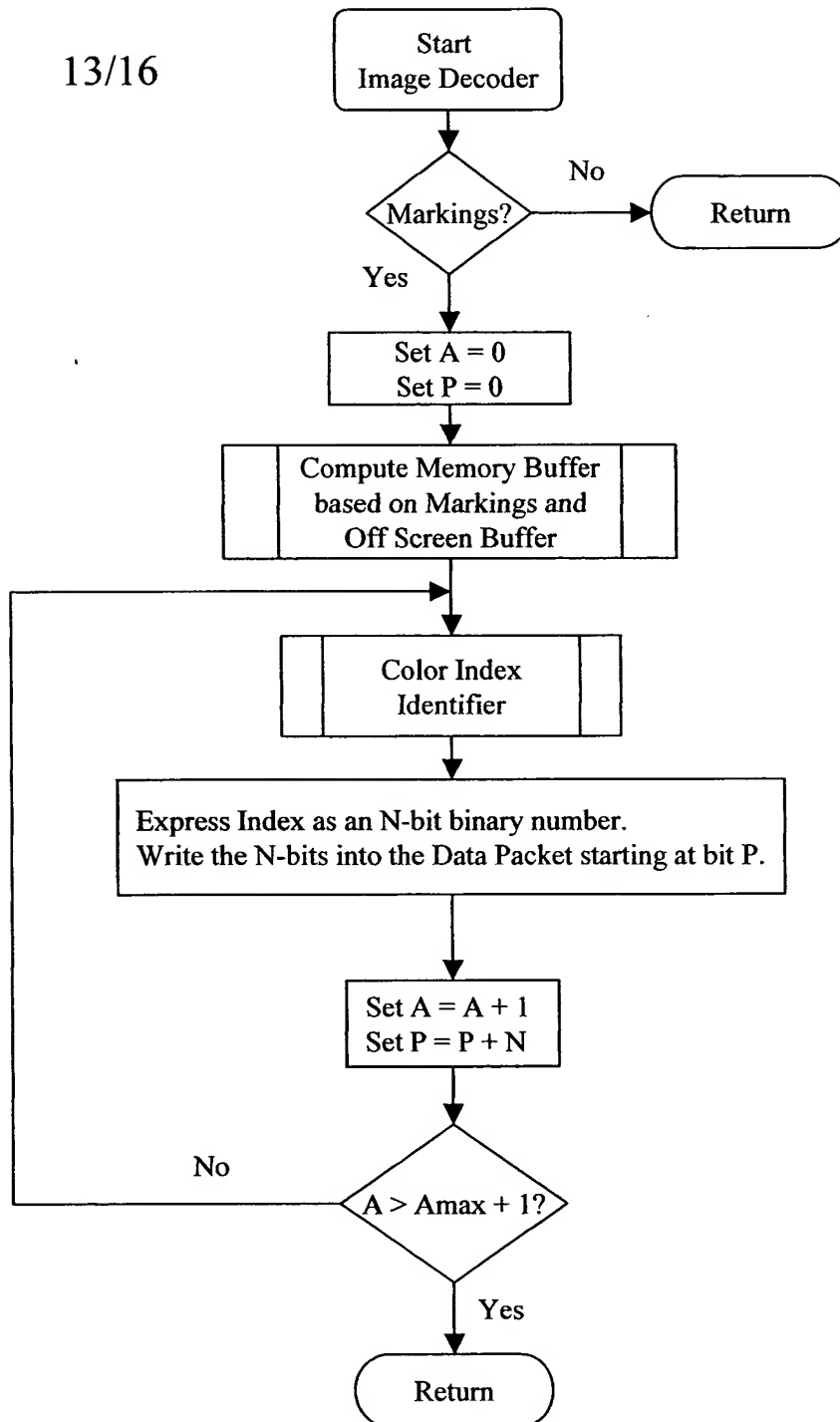


Fig. 11b

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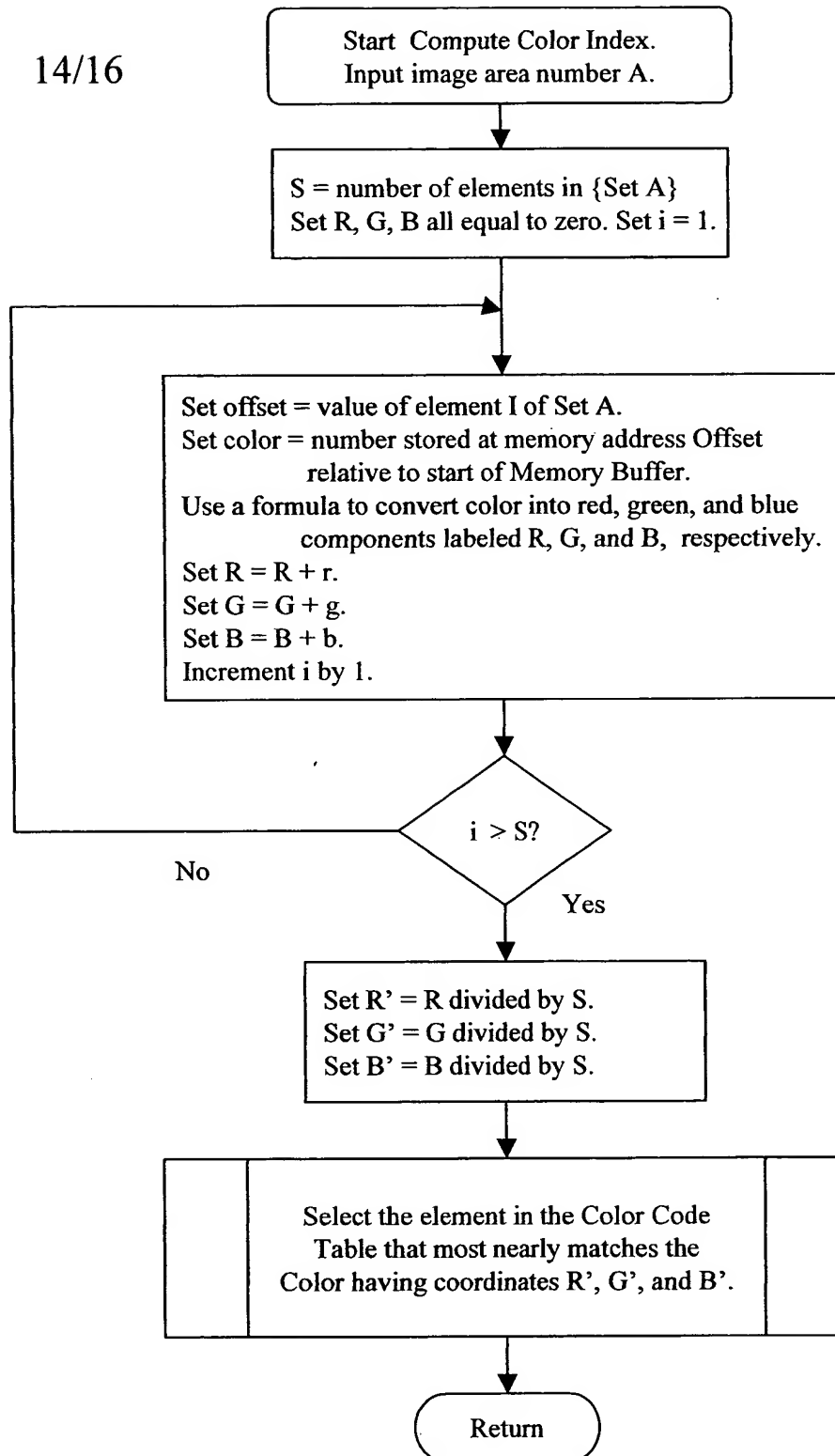


Fig. 12

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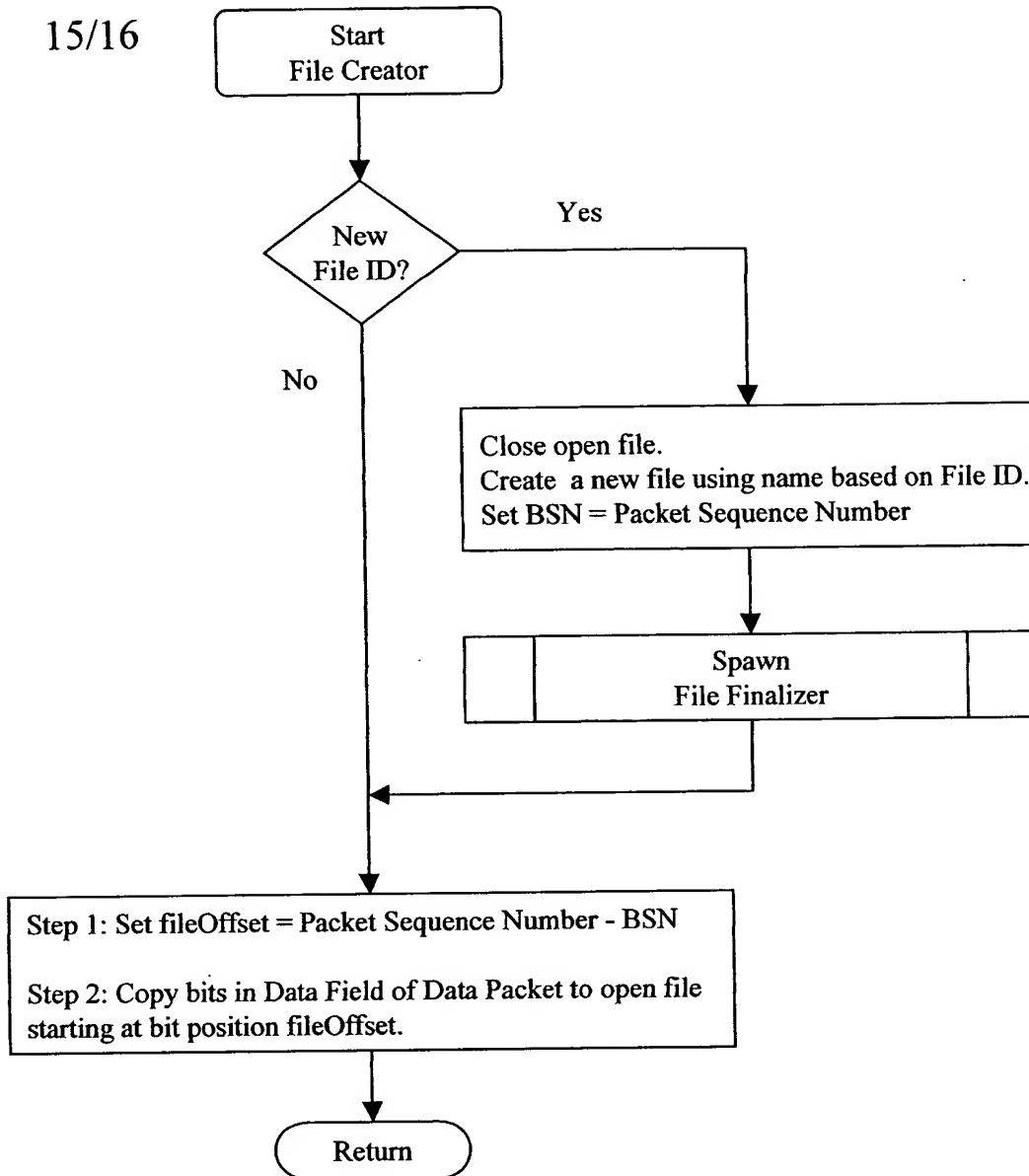


Fig. 13

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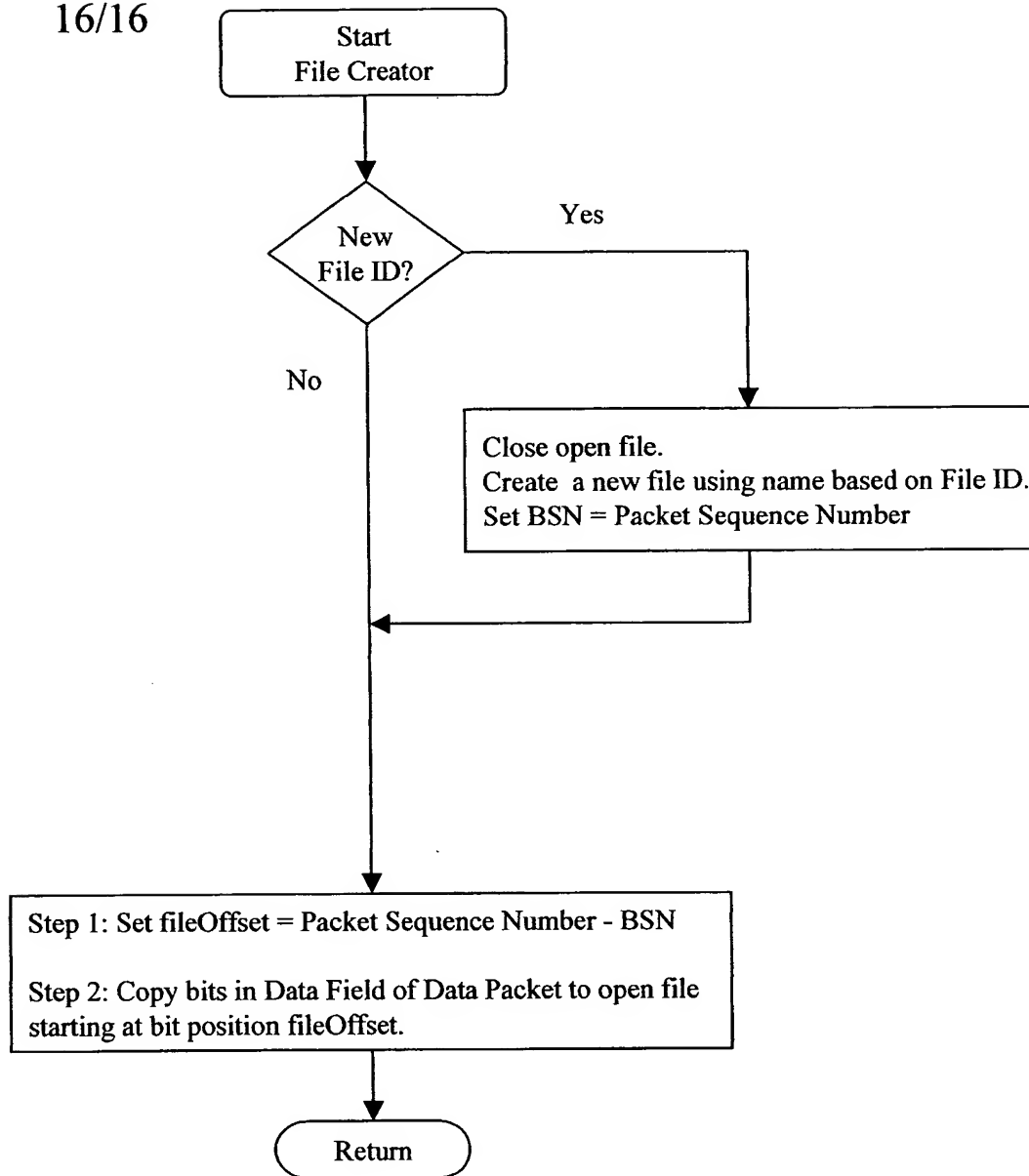


Fig. 13a